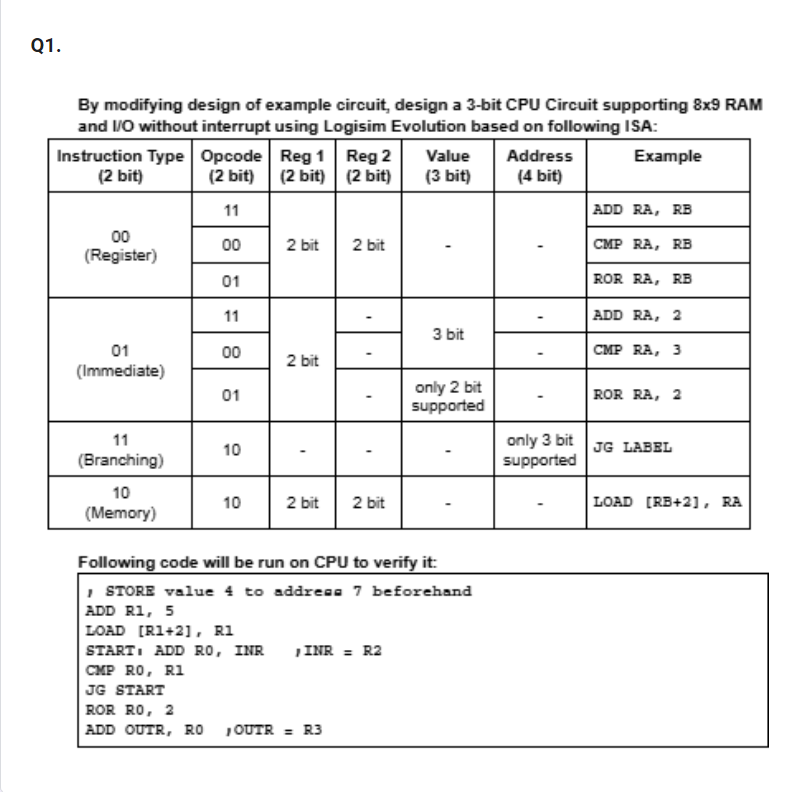
**+Roll No: 2003154**

**Lab Final**

**Lab Task Q[1]**

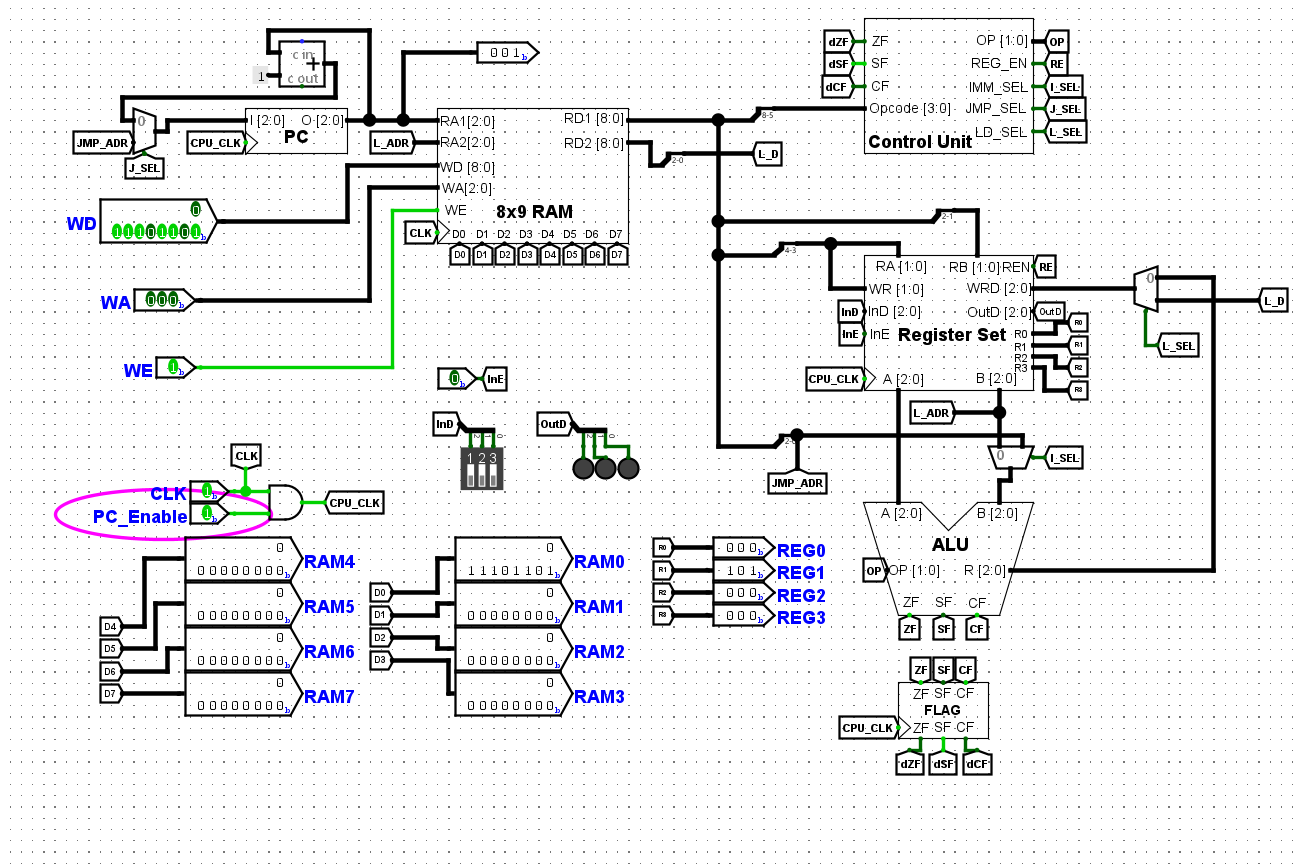
**Question:**

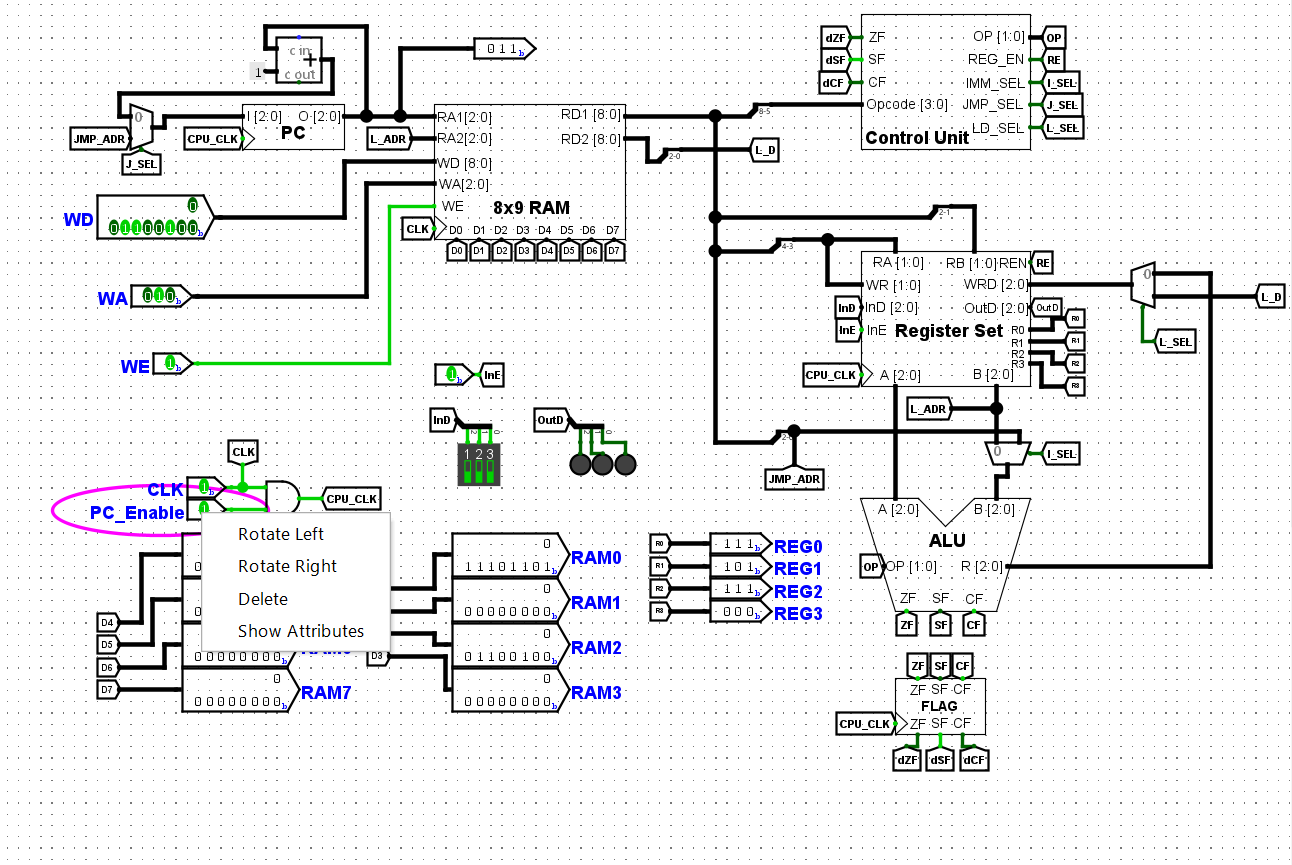
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**Solution (Code/Add all circuits diagrams):**

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**Output/Simulation Result (Screenshot/SnapShot):**

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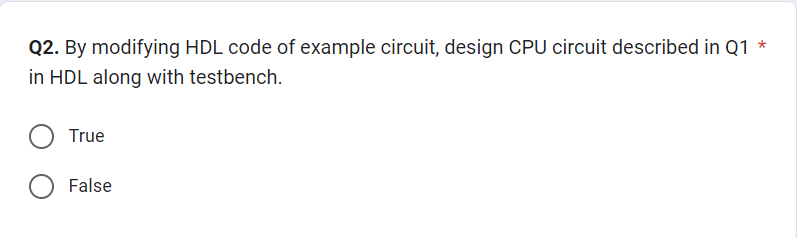
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**Roll No: 2003154**

**Lab Final**

**Lab Task Q[2]**

**Question:**

****

**Solution (Code/Add all circuits diagrams):**

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|  |

**Output/Simulation Result (Screenshot/SnapShot):**